

Shri Shivaji Maratha Society's

Samaj Bhushan Baburao Alias Appasaheb Jedhe Art's, Commerce & Science College, Pune-02

B.Sc. (Computer Science) Internal Examination, Sept.2016

Electronics I – Digital System Hardware

Date: 21/09/2016

Class: S.Y.B.Sc

Time: 11:30-12:30 pm

Marks: 10

Q.1 Write one or two sentence questions (Any ten).

[1x10=10]

1. State the salient features of Microprocessor 8086.
2. What is hit ratio of cache memory.
3. What is queue & pipeline concept.
4. Which are the two modes used in 8086 microprocessors.
5. State the various names of Segment registers in 8086.
6. Write the main parts in CPU organization.
7. State any two advantages of serial port communication.
8. What is difference between RISC & CISC instructions.
9. What is flag register & explain the carry flag.
10. State the various modes of data transfer.
11. What is DMA controller & explain its main role.
12. What is Von Neumann Architecture.

Best of Luck
